

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) An active matrix display device comprising an array of pixels a set of row conductors through which rows of pixels are selected, a set of column conductors through which data signals are supplied to selected pixels, each pixel comprising a plurality of sub pixels which sub pixels are each associated with a respective switching transistor for controlling the supply of a data signal to the sub pixel,

wherein the plurality of sub pixels of a pixel are coupled to a column conductor associated with the pixel via a common switching transistor through which data signals are supplied to the sub pixels, and wherein the device is operable in a first mode in which the plurality of sub-pixels of a pixel are addressed simultaneously with a data signal and in a second mode in which the sub pixels ~~(P1-P4)~~ of a pixel are addressed individually with respective data signals.

2. (Currently amended) A-The display device according to claim 1, wherein the device comprises drive means for providing data signals to the column conductors and switching signals to the row conductors, and wherein the drive means is operable in the first mode to switch the switching transistors associated with the sub pixels of a pixel at the same time so as to supply a data signal on the associated column conductor to each sub pixel, and wherein the drive means is operable in the second mode to switch the switching transistors associated with the sub pixels of the pixel selectively in sequence such that data signals on the associated column conductor are supplied to respective sub pixels.

3. (Currently amended) A-The display device according to claim 1, wherein the sub pixels of a pixel are connected in serial manner with the input terminal of the switching transistor associated with the first sub pixel of the series being connected to the associated column address conductor and with the input terminal of the switching transistor associated with each of the other sub pixels in the series being connected to the output terminal of the switching transistor associated with the preceding sub pixel in the series.

4. (Currently amended) ~~A~~The display device according to claim 1, wherein the sub pixels of a pixel are connected in parallel manner with the input terminal of the switching transistor associated with one sub pixel being connected to the associated column address conductor and with the input terminals of the switching transistors associated with the other sub pixels being connected to the output terminal of the switching transistor associated with the one pixel.

5. (Currently amended) ~~A~~The display device according to ~~any one~~ of claim 1, wherein the control electrodes of the switching transistors associated with the sub pixels of a pixel are connected to respective different row conductors.

6. (Currently amended) ~~A~~The display device according to ~~any one~~ of claim 1, wherein each pixel ~~(P)~~ comprises first and second sub pixels, wherein the control electrodes of the switching transistors associated with the first and second sub pixels of a pixel are connected to first and second row conductors respectively,

wherein, for each pixel, the input of the switching transistor associated with the first sub pixel is connected to the associated

column conductor and the input of the switching transistor associated with the second sub pixel is connected to the output of the switching transistor associated with the first sub pixel,

wherein the first row conductor connected to one pixel is connected also to the control electrode of the switching transistor associated with the second sub pixel of another pixel connected to the associated column conductor, and

wherein the second row conductor connected to the one pixel is connected also to the control electrode of the switching transistor associated with the first sub pixel of a further pixel connected to the associated column address conductor.

7. (Currently amended) A-The display device according to claim 1, wherein the sub pixels comprise liquid crystal picture elements connected to the outputs of their associated switching transistor.

8. (Currently amended) A-The display device according to claim 7, wherein at least two sub pixels of a pixel are of different areas.

9. (New) The display device according to claim 1, wherein the common switching transistor corresponds to the respective switching

transistor of one of the plurality of sub pixels.

10. (New) The display device according to claim 1, wherein each of the common switching transistor and the respective switching transistors comprise an input terminal, an output terminal and a gate terminal, wherein the gate terminal of the common switching transistor is connected to the column conductor associated with the pixel and the output terminal of the common switching transistor is connected to at least one of the input terminals of the respective switching transistors.

11. (New) The display device according to claim 10, wherein the output terminal of the common switching transistor is connected to each of the input terminals of the respective switching transistors.

12. (New) The display device according to claim 10, wherein the output terminal of a first one of the respective switching transistors is connected to the input terminal of a second one of the respective switching transistors.